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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

**TAKAYAMA, Toshio, et al.**

Group Art Unit: **2811**

Serial No.: **10/735,886**

Examiner: **Nitin PAREKH**

Filed: **December 16, 2003**

P.T.O. Confirmation No.: 5693

**FOR: SEMICONDUCTOR DEVICE HAVING A MULTILAYER INTERCONNECTION  
STRUCTURE AND FABRICATION PROCESS THEREOF**

**AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. 1.312**

**MAIL STOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 15, 2006

Sir:

Applicants received a Notice of Allowance dated **July 20, 2006** in connection with the above-identified application. Entry of the following amendments and remarks is respectfully requested.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 9 of this paper.